

UNITED STATES DEPARTMENT OF COMMERCE

Patent and Trademark Office

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APPLICATION NO. FILING DATE		FIRST NAMED INVENTOR			ATTORNEY DOCKET NO.	
8/766,895	12/13/96	DUNNING		D	42390	.P3991
LM02/0825 7			EXAMINER			
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BLAKELY SOKOLOFF TAYLOR & ZAFMAN SEVENTH FLOOR			ART	UNIT	PAPER NUMBER	
2400 WILSHIF	RE BOULEVAR	D		2733		5
OS ANGELES (JH 90025			DATE MA	AILED08/2	25/98

Please find below and/or attached an Office communication concerning this application or proceeding.

Commissioner of Patents and Trademarks

	Applicant (s)
Office Action Commons	08/766,895 Qunning et al
Office Action Summary	Examiner Group Art Unit 2733
The MAILING DATE of this communication app	pears on the cover sheet beneath the correspondence address—
Period for Response	_
A SHORTENED STATUTORY PERIOD FOR RESPONSE MAILING DATE OF THIS COMMUNICATION.	S SET TO EXPIRE MONTH(S) FROM THE
from the mailing date of this communication. - If the period for response specified above is less than thirty (30) of the less than the	FR 1.136(a). In no event, however, may a response be timely filed after SIX (6) MONTH ays, a response within the statutory minimum of thirty (30) days will be considered timely default, expire SIX (6) MONTHS from the mailing date of this communication. will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
Status ,	
Responsive to communication(s) filed on	198
This action is FINAL.	
	ept for formal matters, prosecution as to the merits is closed in 1935 C.D. 1 1; 453 O.G. 213.
Disposition of Claims	
Ø Claim(s)	is/are pending in the application.
·	is/are withdrawn from consideration.
□ Claim(s)	
d Claim(s) 1 − 27	
□ Claim(s)	•
	•
☐ Claim(s)	are subject to restriction or election
☐ Claim(s)————————————————————————————————————	are subject to restriction or election requirement.
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Application Papers	requirement.
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U. S. Patent and Trademark Office PTO-326 (Rev. 3-97)

*U.S. GPO: 1997-417-381/62710 Part of Paper No._____

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DETAILED ACTION

Claim Rejections - 35 U.S.C. § 103

1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(f) or (g) prior art under 35 U.S.C. 103(a).

2. Claims 1-16 are rejected under 35 U.S.C. 103(a) as being unpatentable over Applicants' admitted prior art of figure 3 in view of Huang et al (USP 5,442,474).

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Applicants' admitted prior art of figure 3 shows a reception at a switch of a packet of binary digital signals including encoded binary digital signals used to route the packet through a network. Applicants' admitted prior art of figure 3 differs from the claims in that Applicants' admitted prior art does not teach the copying of the encoded binary digital signals (headers) used to route the packet through the network for decoding the encoded binary digital signals (headers). However, such a feature is taught by Huang. Specifically, Huang teaches that encoded binary digital signals (headers and/or routing bits) for routing determination are copied for decoding of headers and routing bits (see the copying of headers and routing bits by circuits 510 and 520) to correctly route of the packet. Huang's copying circuit performs the copying function in parallel with the reception of the incoming packets and thus reduces processing time and improves routing speed in the switch. Hence, it would have been obvious to one skilled in the art at the time the invention was made to apply Huang's teaching of copying the headers and/or routing bits for decoding headers/routing bits in the prior art system of Applicants' admitted prior art with the motivation being to reduce processing time and, thus, improve routing speed in the switch.

3. Claims 1-16 are rejected under 35 U.S.C. 103(a) as being unpatentable over Judd et al. (USP 5,465,251) in view of Huang et al (USP 5,442,474).

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Judd teaches the receiving at a switch (see node 450 in figure 13) of a packet of binary digital signals including encoded binary digital signals (headers) used to route the packet through a network. Judd differs from the claims in that Judd does not teach the copying of the encoded binary digital signals (headers) used to route the packet through the network for decoding the encoded binary digital signals (headers). However, such a feature is taught by Huang. Specifically, Huang teaches that encoded binary digital signals (headers and/or routing bits) contains routing information are copied for the decoding of headers and routing bits (see the copying of headers and routing bits by circuits 510 and 520) to correctly route the packet. Huang's parallel copying structure reduces processing time and, thus, improves routing speed in the switch. Hence, it would have been obvious to one skilled in the art at the time the invention was made to apply Huang's teaching of copying the headers and/or routing bits for decoding headers/routing bits in Judd's system with the motivation being to reduce processing time and, thus, improve routing speed in the switch.

4. Claims 17-27 are rejected under 35 U.S.C. 103(a) as being unpatentable over May et al. (USP 5,422,881) in view of Huang et al (USP 5,442,474).

May teaches the reception of packets containing serial data format with "clock-with-data" encoding (see col. 5, lines 9-50). May fails to teach the specifying of a

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route without decoding. However, Huang teaches a format of routing bits in the packet, which enables the specifying of a route without decoding. Hence, it would have been obvious to one skilled in the art at the time the invention was made to apply Huang's teaching of using the routing bits for specifying a route in May's system with the motivation being to reduce processing time and, thus, improve routing speed in the switch.

Regarding claims 25-27, May's circuit La can be considered as a network interface component since it provides an interface between switch 10 and a host device or between switch 10 and another switch.

Applicants' arguments filed June 1, 1998 have been fully considered but they 5. are not persuasive.

With respect to claims 1-16, in response to Applicant's argument that the prior art references do not teach the limitation of "encoded binary digital signals used to route packets through a network" as described in lines 3-14 of page 10 of the specification, Applicant misinterprets the principle that claims are interpreted in the light of the specification. Although these elements in the above-mentioned passage in the specification are found as examples or embodiments in the specification, they were not claimed explicitly. Nor were the words that are used in the claims defined in the specification to require these limitations. A reading of the specification

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provides no evidence to indicate that these limitations must be imported into the claims to give meaning to disputed terms.

With respect to claims 17-27, in response to Applicant's argument that Huang patent fails to teach the limitation of "encoded binary digital signals specifying of a route without decoding" as described in the specification, applicant misinterprets the principle that claims are interpreted in the light of the specification. Although a specific description of the above limitation is found as examples or embodiments in the specification, they were not claimed explicitly. Nor were the words that are used in the claims defined in the specification to require these limitations. A reading of the specification provides no evidence to indicate that these limitations must be imported into the claims to give meaning to disputed terms.

Furthermore, in response to Applicant's argument that Huang patent fails to teach the limitation of,

In response to Applicant's argument that does not include certain features of Applicant's invention, the limitations on which the Applicant relies (i.e., "to send these signal [sic] serially it is desirable to encode the bit stream so that at least a selected number and a selected frequency of bit transitions occur in the bit stream" and a specific description of encoding as described in lines 4-5 of page 3 and in page 9 respectively of the specification) are not stated in the claims. Therefore, it is, irrelevant whether the reference includes those features or not.

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THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time 6.

policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE

MONTHS from the mailing date of this action. In the event a first reply is filed within

TWO MONTHS of the mailing date of this final action and the advisory action is not

mailed until after the end of the THREE-MONTH shortened statutory period, then the

shortened statutory period will expire on the date the advisory action is mailed, and

any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing

date of the advisory action. In no event, however, will the statutory period for reply

expire later than SIX MONTHS from the mailing date of this final action.

7. Any response to this final action should be mailed to:

Box AF

Commissioner of Patents and Trademarks

Washington, D.C. 20231

or faxed to:

(703) 308-9051, (for formal communications; please mark "EXPEDITED

PROCEDURE")

Or:

(703) 305-9508 (for informal or draft communications, please label

"PROPOSED" or "DRAFT")

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Hand-delivered responses should be brought to Crystal Park II, 2121 Crystal Drive, Arlington. VA., Sixth Floor (Receptionist).

8. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Huy D. Vu whose telephone number is (703) 308-6602. The examiner can normally be reached on Tuesday - Friday from 8:00 a.m. to 5:30 p.m. The examiner can also be reached on alternate Fridays.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jason Chan, can be reached on (703) 305-4729.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (703) 305-4700.

Huy D. Vu

Primary Examiner

August 16, 1998